DEC 14 2007

December 7, 2007

Commissioner for Patents

P. O. Box 1450

Alexandria, VA 22313-1450

Attn:

Art Unit 2891 - Examiner David A Zarneke

FROM:

Stephen B. Ackerman, Reg. No. 37,761

28 Davis Avenue

Poughkeepsie, N.Y. 12603

SUBJECT:

Serial #:

09/837,007

File Date:

April 18, 2001

Inventor: Examiner: M.S. Lin, et al. David A. Zarneke

Art Unit:

2891

Title:

A Structure and Manufacturing Method of a Chip Scale

Package

## PRELIMINARY AMENDMENT

Dear Sir:

Please accept this Preliminary Amendment concerning the above-identified application for patent.

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on Dec. 11, 2007.

Stephen B. Ackerman, Reg # 37,761

Signature

Date 10/11/07